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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Seong Min Seo, Young Suk Chung, Jon Sik Paek, Jae Hun Ku, Jae Hak

Yee

Assignee: Amkor Technology, Inc.

Title: Semiconductor Package Including Stacked Chips

Serial No.: 09/816,599 Filing Date: March 23, 200

Not Yet Assigned Group Art Unit: 2811

Docket No.: AB-1111 US

San Jose, California December 6, 2001

COMMISSIONER FOR PATENTS Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT AUG 2 0 2002

Technology Center 2600

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Dear Sir:

Examiner:

Pursuant to 37 C.F.R. § 1.56, § 1.97 and § 1.98, Applicants wish to call the document listed on the accompanying PTO Form-1449 to the Examiner's attention. A copy of this document is enclosed.

UNDER 37 CFR § 1.97(b)

Citation of these documents shall not be construed as:

- 1. an admission that the documents are necessarily prior art with respect to the instant invention;
 - 2. a representation that a search has been made, or
- 3. an admission that the information cited herein is, or is considered to be, material to patentability as defined in § 1.56(b).

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Applicants submit that no fee is required. If, however, a fee is necessary under 37 C.F.R. 1.97(c), then such fee and any other appropriate fee may be charged to Deposit Account No. 19-2386.

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, Washington, D.C.

20231, or December 6, 2001

Attorney for Applicants

Date of Signature

Respectfully submitted,

James E. Parsons

Attorney for Applicants

Reg. No. 34,691

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT					Applicants				
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